!PADS-POWERPCB-V9.0-MILS! NETLIST FILE FROM PADS LOGIC VVX.2.1

\*REMARK\* default.sch -- Sun Sep 03 22:02:17 2017

\*REMARK\*

\*PCB\* GENERAL PARAMETERS OF THE PCB DESIGN

MAXIMUMLAYER 2 Maximum routing layer

\*PART\* ITEMS

U1 NE555\_SOIC8\_MJ@SOIC8\_MJ

C1 CAP0805@0805

C2 CAP-CK05@CK05

R1 RES-1/4W@R1/4W

R2 RES0805@0805

R3 RES0805@0805

D1 LED@LED

J1 HEADER02@SIP-2P

\*NET\*

\*SIGNAL\* +5V

J1.1 C2.1 U1.4 U1.8 R2.1

\*SIGNAL\* GND

J1.2 C2.2 C1.1 D1.K U1.1

\*SIGNAL\* $$$4010

C1.2 U1.2

\*SIGNAL\* $$$4158

D1.A R1.1

\*SIGNAL\* $$$4160

R1.2 U1.3

\*SIGNAL\* $$$4181

U1.6 R3.1

\*SIGNAL\* $$$4187

R3.2 U1.7 R2.2

\*MISC\* MISCELLANEOUS PARAMETERS

\*REMARK\* PARENT\_KEYWORD PARENT\_VALUE

\*REMARK\* [ {

\*REMARK\* CHILD\_KEYWORD CHILD\_VALUE

\*REMARK\* [ CHILD\_KEYWORD CHILD\_VALUE

\*REMARK\* [ {

\*REMARK\* GRAND\_CHILD\_KEYWORD GRAND\_CHILD\_VALUE [...]

\*REMARK\* } ]]

\*REMARK\* } ]

LAYER MILS

{

LAYER 0

{

LAYER\_THICKNESS 0

DIELECTRIC 3.300000

}

LAYER 1

{

LAYER\_NAME Top

LAYER\_TYPE ROUTING

PLANE NONE

ROUTING\_DIRECTION HORIZONTAL

ASSOCIATED\_SILK\_SCREEN Silkscreen Top

ASSOCIATED\_PASTE\_MASK Paste Mask Top

ASSOCIATED\_SOLDER\_MASK Solder Mask Top

ASSOCIATED\_ASSEMBLY Assembly Drawing Top

COMPONENT Y

ROUTABLE Y

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 10

COPPER\_THICKNESS 1.35

DIELECTRIC 4.300000

COST 0

}

LAYER 2

{

LAYER\_NAME Bottom

LAYER\_TYPE ROUTING

PLANE NONE

ROUTING\_DIRECTION VERTICAL

ASSOCIATED\_SILK\_SCREEN Silkscreen Bottom

ASSOCIATED\_PASTE\_MASK Paste Mask Bottom

ASSOCIATED\_SOLDER\_MASK Solder Mask Bottom

ASSOCIATED\_ASSEMBLY Assembly Drawing Bottom

COMPONENT Y

ROUTABLE Y

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 1.35

DIELECTRIC 3.300000

COST 0

}

LAYER 3

{

LAYER\_NAME Layer\_3

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 4

{

LAYER\_NAME Layer\_4

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 5

{

LAYER\_NAME Layer\_5

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 6

{

LAYER\_NAME Layer\_6

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 7

{

LAYER\_NAME Layer\_7

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 8

{

LAYER\_NAME Layer\_8

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 9

{

LAYER\_NAME Layer\_9

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 10

{

LAYER\_NAME Layer\_10

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 11

{

LAYER\_NAME Layer\_11

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 12

{

LAYER\_NAME Layer\_12

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 13

{

LAYER\_NAME Layer\_13

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 14

{

LAYER\_NAME Layer\_14

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 15

{

LAYER\_NAME Layer\_15

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 16

{

LAYER\_NAME Layer\_16

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 17

{

LAYER\_NAME Layer\_17

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 18

{

LAYER\_NAME Layer\_18

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 19

{

LAYER\_NAME Layer\_19

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 20

{

LAYER\_NAME Layer\_20

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 21

{

LAYER\_NAME Solder Mask Top

LAYER\_TYPE SOLDER\_MASK

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 22

{

LAYER\_NAME Paste Mask Bottom

LAYER\_TYPE PASTE\_MASK

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 23

{

LAYER\_NAME Paste Mask Top

LAYER\_TYPE PASTE\_MASK

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 24

{

LAYER\_NAME Drill Drawing

LAYER\_TYPE DRILL

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 25

{

LAYER\_NAME Layer\_25

LAYER\_TYPE UNASSIGNED

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 26

{

LAYER\_NAME Silkscreen Top

LAYER\_TYPE SILK\_SCREEN

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 27

{

LAYER\_NAME Assembly Drawing Top

LAYER\_TYPE ASSEMBLY

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 28

{

LAYER\_NAME Solder Mask Bottom

LAYER\_TYPE SOLDER\_MASK

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 29

{

LAYER\_NAME Silkscreen Bottom

LAYER\_TYPE SILK\_SCREEN

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

LAYER 30

{

LAYER\_NAME Assembly Drawing Bottom

LAYER\_TYPE ASSEMBLY

PLANE NONE

ROUTING\_DIRECTION NO\_PREFERENCE

VISIBLE Y

SELECTABLE Y

ENABLED Y

LAYER\_THICKNESS 0

COPPER\_THICKNESS 0

DIELECTRIC 0.000000

COST 0

}

}

\*REMARK\* PARENT\_KEYWORD PARENT\_VALUE

\*REMARK\* [ {

\*REMARK\* CHILD\_KEYWORD CHILD\_VALUE

\*REMARK\* [ CHILD\_KEYWORD CHILD\_VALUE

\*REMARK\* [ {

\*REMARK\* GRAND\_CHILD\_KEYWORD GRAND\_CHILD\_VALUE [...]

\*REMARK\* } ]]

\*REMARK\* } ]

RULES\_SECTION MILS

{

NET\_CLASS DATA

DESIGN RULES

{

RULE\_SET (1)

{

FOR :

{

DEFAULT :

}

AGAINST :

{

DEFAULT :

}

LAYER 0

CLEARANCE\_RULE :

{

TRACK\_TO\_TRACK 6

VIA\_TO\_TRACK 6

VIA\_TO\_VIA 6

PAD\_TO\_TRACK 6

PAD\_TO\_VIA 6

PAD\_TO\_PAD 6

SMD\_TO\_TRACK 6

SMD\_TO\_VIA 6

SMD\_TO\_PAD 6

SMD\_TO\_SMD 6

COPPER\_TO\_TRACK 6

COPPER\_TO\_VIA 6

COPPER\_TO\_PAD 6

COPPER\_TO\_SMD 6

COPPER\_TO\_COPPER 6

TEXT\_TO\_TRACK 6

TEXT\_TO\_VIA 6

TEXT\_TO\_PAD 6

TEXT\_TO\_SMD 6

OUTLINE\_TO\_TRACK 6

OUTLINE\_TO\_VIA 6

OUTLINE\_TO\_PAD 6

OUTLINE\_TO\_SMD 6

OUTLINE\_TO\_COPPER 6

DRILL\_TO\_TRACK 6

DRILL\_TO\_VIA 6

DRILL\_TO\_PAD 6

DRILL\_TO\_SMD 6

DRILL\_TO\_COPPER 6

SAME\_NET\_SMD\_TO\_VIA 6

SAME\_NET\_SMD\_TO\_CRN 6

SAME\_NET\_VIA\_TO\_VIA 6

SAME\_NET\_PAD\_TO\_CRN 6

MIN\_TRACK\_WIDTH 6

REC\_TRACK\_WIDTH 6

MAX\_TRACK\_WIDTH 6

DRILL\_TO\_DRILL 6

BODY\_TO\_BODY 6

SAME\_NET\_TRACK\_TO\_CRN 6

}

}

RULE\_SET (2)

{

FOR :

{

DEFAULT :

}

AGAINST :

{

DEFAULT :

}

LAYER 0

ROUTE\_RULE :

{

LENGTH\_MINIMIZATION\_TYPE 1

TRACE\_SHARE Y

VIA\_SHARE Y

AUTO\_ROUTE Y

RIPUP Y

SHOVE Y

ROUTE\_PRIORITY 3

MAX\_NUMBER\_OF\_VIAS -1

VALID\_LAYER 1

VALID\_LAYER 2

VALID\_VIA\_TYPE \*USE\_CURRENT\*

}

}

RULE\_SET (3)

{

FOR :

{

DEFAULT :

}

AGAINST :

{

DEFAULT :

}

LAYER 0

HIGH\_SPEED\_RULE :

{

MIN\_LENGTH 0

MAX\_LENGTH 50000

STUB\_LENGTH 0

PARALLEL\_LENGTH 1000

PARALLEL\_GAP 200

TANDEM\_LENGTH 1000

TANDEM\_GAP 200

MIN\_DELAY 0.000000

MAX\_DELAY 10.000000

MIN\_CAPACITANCE 0.000000

MAX\_CAPACITANCE 10.000000

MIN\_IMPEDANCE 50.000000

MAX\_IMPEDANCE 150.000000

SHIELD\_NET \*

SHIELD\_GAP 200

MATCH\_LENGTH\_TOLERANCE 200

}

}

}

}

\*MISC\* MISCELLANEOUS PARAMETERS

ATTRIBUTE VALUES

{

PART C1

{

"Sim.Analog.Model"

"Sim.Analog.Order" Model$

"Sim.Analog.Prefix"

"Description" SURFACE MOUNT CAPACITOR 0.048 X 0.079 INCHES

"Part Number"

"Manufacturer #1" IPC SM-782 STD.

"Value" 10 uF

"Tolerance"

"Voltage Rating"

}

PART C2

{

"Sim.Analog.Model"

"Sim.Analog.Order" Model$

"Sim.Analog.Prefix" C

"Description" RADIAL CERAMIC CAPACITOR, MIL-SPEC SIZE CK05

"Part Number" CK05BXNNNN

"Value" 0.01 uF

"Tolerance" ???

"Voltage Rating" ???

"Manufacturer #1" KEMET

"Cost"

}

PART R1

{

"Sim.Analog.Model"

"Sim.Analog.Order" Model$

"Sim.Analog.Prefix" R

"Description" RES BODY:100 CENTERS:500

"Part Number"

"Value" 330

"Manufacturer #1"

"Tolerance"

}

PART R2

{

"Sim.Analog.Model"

"Sim.Analog.Order" Model$

"Sim.Analog.Prefix" R

"Description" SURFACE MOUNT RESISTOR 0.048 X 0.079 INCHES

"Part Number"

"Manufacturer #1" IPC SM-782 STD.

"Value" 51K

"Tolerance"

}

PART R3

{

"Sim.Analog.Model"

"Sim.Analog.Order" Model$

"Sim.Analog.Prefix" R

"Description" SURFACE MOUNT RESISTOR 0.048 X 0.079 INCHES

"Part Number"

"Manufacturer #1" IPC SM-782 STD.

"Value" 51K

"Tolerance"

}

PART D1

{

"Sim.Analog.Model"

"Sim.Analog.Order" Model$

"Sim.Analog.Prefix" D

"Description" LIGHT EMITTING DIODE

"Color" ???

"Part Number"

"Manufacturer #1"

}

PART J1

{

"Description" TWO PIN INLINE HEADER; 100 MIL CENTERS

"Part Number"

"Manufacturer #1"

"Cost"

}

}

\*END\* OF ASCII OUTPUT FILE